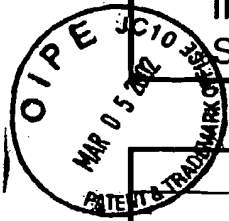


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INFORMATION DISCLOSURE STATEMENT BY APPLICANT

[illegible]

Examiner Initials	Foreign Patent Document		Country	Date of Publication (MM-DD-YYYY)	Translation	
	Number	Kind Code (if known)			Yes	no

Examiner Initials	Include name of author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.		
DA	Chameleon Systems, Inc., Product Overview sheet for CS2000 Reconfigurable Communications Processor, © 2000		
	Chameleon Systems, Inc., Family Product Brief for CS2000 Reconfigurable Communications Processor, Advance Produce Information, © 2000		
	Glaskowsky, "Chameleon Crosses CPU, FPGA," Microdesign Resources, June 12, 2000.		
	Dave Bursky [Editor-in-Chief], "Comm Processor Adjusts For Task At Hand," Electronic Design, May 15, 2000 pg. 66 et seq.		
	Graham Mostyn, "Benefits of Reconfigurable Processors for Communication System Design," Communication Systems Design, January 2000		
	"Reconfigurables hit comms processing," Electronic Engineering Times, May 22, 2000		
	Crista Souza, "Semiconductors: Chameleon a quick-change artist," Electronic Buyers' News, May 22, 2000		
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EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. SEND TO: Assistant Commissioner for Patents, Washington, D.C. 20231.